IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: Samuel A. Gebremariam

Date: March 7, 2003

Clouting

Samuel A. Gebremariam

3117103 In re Patent Application of:) Akira ASAI et al. Serial No. 10/031,445 Filed: January 22, 2002 For:

BIPOLAR TRANSISTOR AND

FABRICATION METHOD THEREOF

CERTIFICATE OF MAILING

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Deborah Movahhedi

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

The Examiner's action dated February 11, 2003, has been received and its contents carefully noted. In view thereof, Applicants hereby elect, without traverse, Group I, Claims 1-5, drawn to a semiconductor integrated circuit device, for prosecution in the instant application. consideration and allowance of the instant application are now respectfully requested.

Respectfully submitted,

Donald R. Studebaker Registration No. 32,815

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TO A NICHATITY A T		Application Number		10/031,445		
TRANSMITTAL FORM			Filing Date		January 22, 2002	
(to be used for all correspondence after initial filing)			First Named Inventor		Akira ASAI et al.	
		Group Art Unit		2811		
			Examiner Name		Samuel A. GEBREMARIAM	
Total Number of Pages in This Submission		Attorney Docket Number			740819-734	
ENCLOSURES (check all that apply)						
Fee Transmittal Form		Assignment Papers			After Allowance Communication to Group	
Fee Attached		_	Application)		Appeal Communication to Board of	
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After Final					(Appeal Notice, Brief, Reply Brief)	
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Affidavits/declaration(s)					Application Data Sheet	
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·		required or credit any overpa above identified docket num		ayme	nts to Deposit Account No. 19-2380 for the	
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT						
Firm						
or Individual name	Donald R. Studebaker, Reg. No. 32,815 Nixon Peabody LLP					
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